

#### PATENT APPLICATION

# THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q77571

Mohamed Shaker SARWARY, et al.

Appln. No.: 10/695,803

Group Art Unit: 2825

Confirmation No.: 6876

Examiner: Sun J. LIN

Filed: October 30, 2003

For:

METHOD FOR CLOCK SYNCHRONIZATION VALIDATION IN INTEGRATED

**CIRCUIT DESIGN** 

### AMENDMENT UNDER 37 C.F.R. § 1.111

## MAIL STOP AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated October 26, 2005, please amend the aboveidentified application as follows on the accompanying pages.

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